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Abstract
Refer to excogitation of radio and mobile communications need to provide stable oscillations at specific frequencies, Up conversion and Down conversion can help to establish mixing condition in receiver or transmitter process. Then, VCO is the critical part in receiving and transmitting process. In this thesis, we are researching and investigating VCO 5.8 GHz.
In this thesis, to simulate and design these circuits by using ADS with TSMC SiGe BiCMOS 0.35μm process, in order to optimize circuits function. The VCO deign by using negative resistance for traditional LC tank. The simulated VCOs output frequency ranges are 5.8 GHz and 5.4 GHz, and the phase noises at an offset frequency of 1 MHz are -112 dBc/Hz.
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